

HIGH ISOLATION VOLTAGE DARLINGTON TRANSISTOR SOP MULTI PHOTOCOUPLER SERIES

NEPOC Series

DESCRIPTION

The PS2702-1 is an optically coupled isolator containing a GaAs light emitting diode and an NPN silicon darlingtonconnected phototransistor.

This is mounted in a plastic SOP (Small Out-line Package) for high density applications.

This package has shield effect to cut off ambient light.

FEATURES

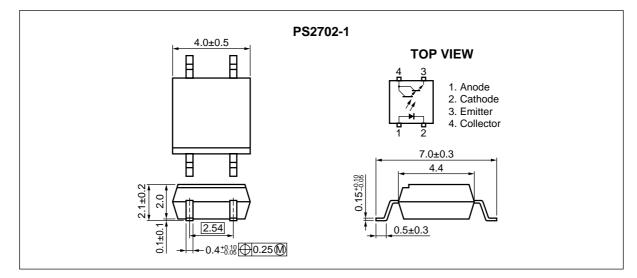
- High current transfer ratio (CTR = 2 000% TYP.)
- High isolation voltage (BV = 3 750 Vr.m.s.)
- Small and thin (SOP) package
- High-speed switching (tr, tf = 200 μ s TYP.)
- Ordering number of taping product: PS2702-1-F3, F4
- Safety standards
 - UL approved: File No. E72422
 - BSI approved: File No. 8219/8220
 - CSA approved: File No. CA 101391
 - DIN EN60747-5-2 (VDE0884 Part2) approved (Option)

APPLICATIONS

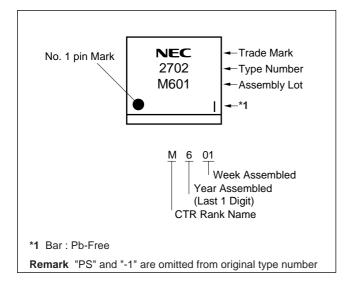
- Hybrid IC
- Telephone/FAX
- FA/OA equipment
- Programmable logic controllers

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PACKAGE DIMENSIONS (in millimeters)



★ MARKING EXAMPLE



★ ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS2702-1	PS2702-1-A	Pb-Free	Magazine case 100 pcs	Standard products	PS2702-1
PS2702-1-F3	PS2702-1-F3-A		Embossed Tape 3 500 pcs/reel	(UL, BSI, CSA	
PS2702-1-F4	PS2702-1-F4-A			approved)	
PS2702-1-V	PS2702-1-V-A		Magazine case 100 pcs	DIN EN60747-5-2	
PS2702-1-V-F3	PS2702-1-V-F3-A		Embossed Tape 3 500 pcs/reel	(VDE0884 Part2)	
PS2702-1-V-F4	PS2702-1-V-F4-A			Approved (Option)	

*1 For the application of the Safety Standard, following part number should be used.

Parameter		Symbol	Ratings	Unit
Diode	Forward Current (DC)	lF	50	mA
	Reverse Voltage	VR	6.0	V
	Power Dissipation Derating	⊿Po/°C	0.8	mW/°C
	Power Dissipation	Po	80	mW
	Peak Forward Current *1	IFP	1	А
Transistor	Collector to Emitter Voltage	Vceo	40	V
	Emitter to Collector Voltage	Veco	6	V
	Collector Current	lc	200	mA
	Power Dissipation Derating	⊿Pc/°C	1.5	mW/°C
	Power Dissipation	Pc	150	mW
Isolation Voltage *2		BV	3 750	Vr.m.s.
Operating Ambient Temperature		TA	–55 to +100	°C
Storage Temperature		Tstg	–55 to +150	°C

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

*1 PW = 100 *µ*s, Duty Cycle = 1%

*2 AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output Pins 1-2 shorted together, 3-4 shorted together.

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	IF = 5 mA		1.1	1.4	V
	Reverse Current	Ir	V _R = 5 V			5	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz		30		pF
Transistor	Collector to Emitter Dark Current	Iceo	IF = 0 mA, Vce = 40 V			400	nA
Coupled	Current Transfer Ratio	CTR	IF = 1 mA, VCE = 2 V	200	2 000		%
	Collector Saturation Voltage	Vce (sat)	I⊧ = 1 mA, lc = 2 mA			1.0	V
	Isolation Resistance	Ri-o	VI-O = 1 kVDC	10 ¹¹			Ω
	Isolation Capacitance	CI-0	V = 0 V, f = 1 MHz		0.4		pF
	Rise Time *2	tr	$V_{CC} = 5 \text{ V}, \text{ Ic} = 2 \text{ mA}, \text{ R}_{L} = 100 \Omega$		200		μS
	Fall Time *2	tr			200		

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

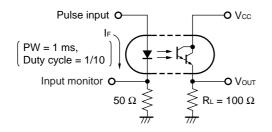
*1 CTR rank

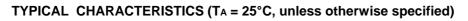
K: 2 000 to (%)

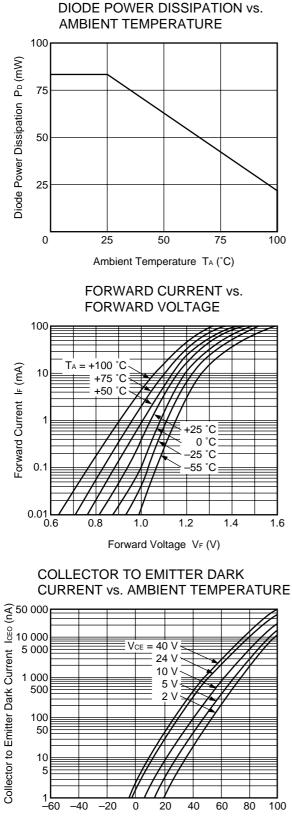
L: 700 to 3 400 (%)

M: 200 to 1 000 (%)

*2 Test circuit for switching time







Ambient Temperature T_A (°C)

Remark The graphs indicate nominal characteristics.

AMBIENT TEMPERATURE 200 Transistor Power Dissipation Pc (mW) 150 1.5 mW/°C 100

50

Ambient Temperature T_A (°C)

75

100

50

0

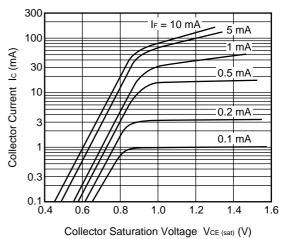
25

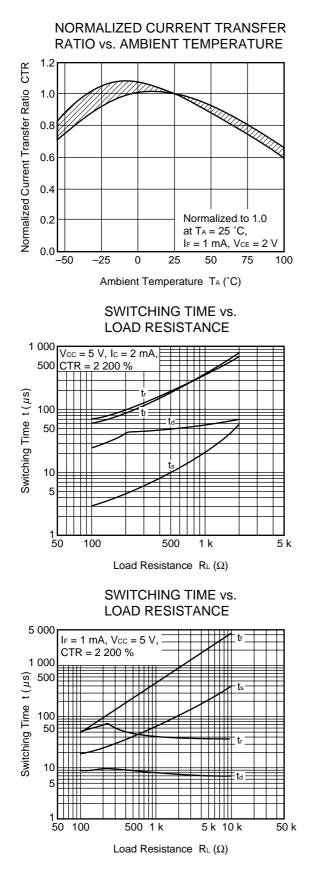
COLLECTOR CURRENT vs.

TRANSISTOR POWER DISSIPATION vs.

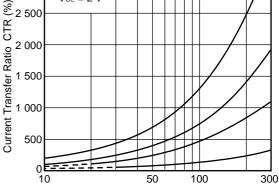
COLLECTOR TO EMITTER VOLTAGE 180 160 lc (mA) 140 120 IF = 5 mA Collector Current 100 2 mA 80 60 40 0.5 mA 20 0 2 4 8 6 10 Collector to Emitter Voltage VCE (V)

COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



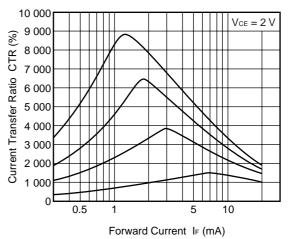


CURRENT TRANSFER RATIO vs. FORWARD CURRENT 3 000 $V_{CE} = 2 V$ 2 500 2 000

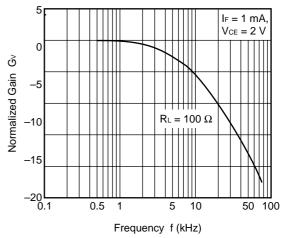


Forward Current IF (μ A)

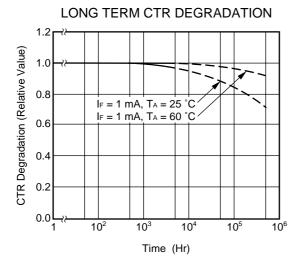
CURRENT TRANSFER RATIO vs. FORWARD CURRENT





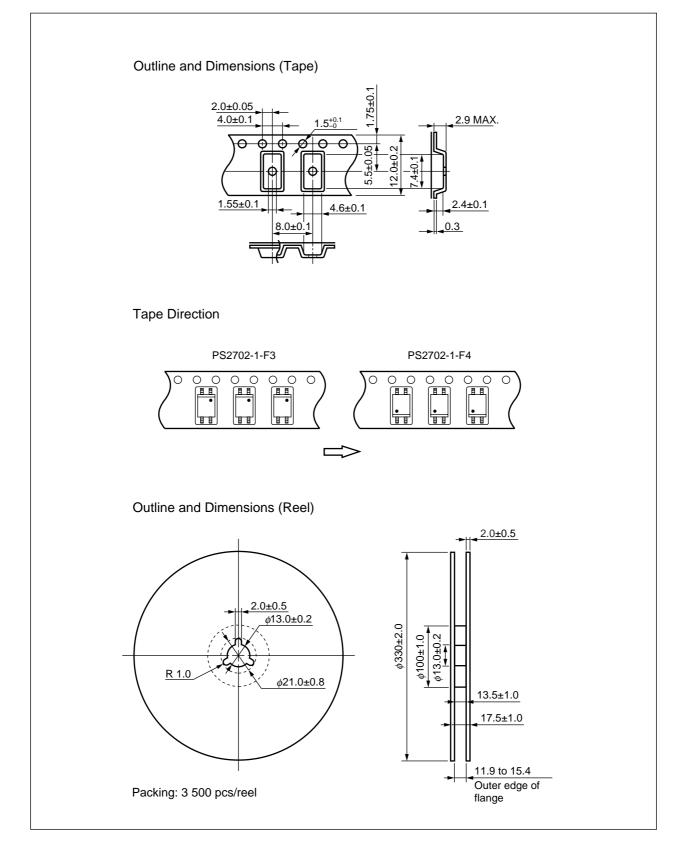


Remark The graphs indicate nominal characteristics.



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TAPING SPECIFICATIONS (in millimeters)



NOTES ON HANDLING

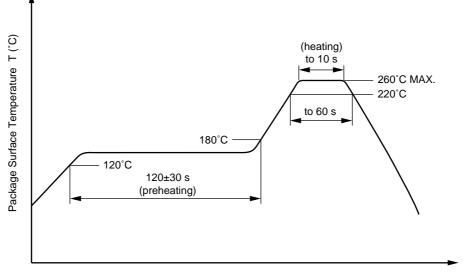
1. Recommended soldering conditions

(1) Infrared reflow soldering

- Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

★ (3) Soldering by soldering iron

 Peak temperature (lead part temperature) 	350°C or below
Time (each pins)	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

(4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

★ 3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (VDE0884)

Parameter	Symbol	Speck	Unit
Application classification (DIN VDE 0109) for rated line voltages \leq 300 Vr.m.s. for rated line voltages \leq 600 Vr.m.s.		IV III	
Climatic test class (DIN IEC 68 Teil 1/09.80)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.2 \times U_{IORM}, P_d < 5 \text{ pC}$	Uiorm Upr	710 850	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices test) U_{pr} = 1.6 \times U_{10\text{RM}}, P_{\text{d}} < 5 \ pC	Upr	1 140	V _{peak}
Highest permissible overvoltage	Utr	6 000	V _{peak}
Degree of pollution (DIN VDE 0109)		2	
Clearance distance		> 5	mm
Creepage distance		> 5	mm
Comparative tracking index (DIN IEC 112/VDE 0303 part 1)	CTI	175	
Material group (DIN VDE 0109)		lll a	
Storage temperature range	Tstg	-55 to +150	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc}$ at $T_A = 25 \text{ °C}$ $V_{IO} = 500 \text{ V dc}$ at T_A MAX. at least 100 °C	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	150 200 300	°C mA mW
Vio = 500 V dc at T _A = 175 °C (Tsi)	Ris MIN.	10 ⁹	Ω

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M8E 00.4-0110

Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or in any way allow it to enter the mouth.

► For further information, please contact

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Subject: Compliance with EU Directives

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CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentratio in CEL	
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)	
Mercury	< 1000 PPM	Not De	etected
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not De	etected
РВВ	< 1000 PPM	Not Detected	
PBDE < 1000 PPM Not Dete		etected	

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In no event shall CEL's liability arising out of such information exceed the total purchase price of the CEL part(s) at issue sold by CEL to customer on an annual basis.

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